**8 Bit division:**

**Code:**

LHLD 2500

XCHG

LHLD 2502

MOV A,E

SUB L

MOV L,A

MOV A,D

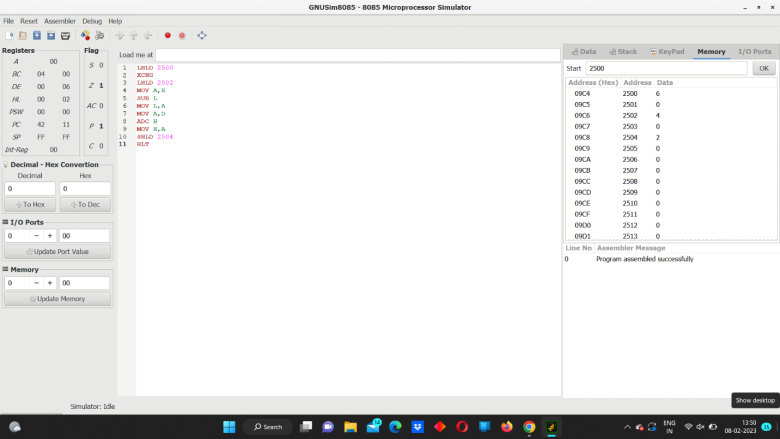
ADC H

MOV H,A

SHLD **2504**

HLT

**OUTPUT:**

s